

Fig. 1A

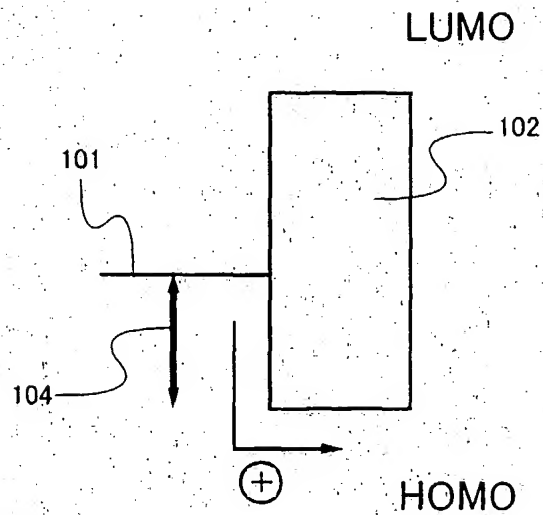


Fig. 1B

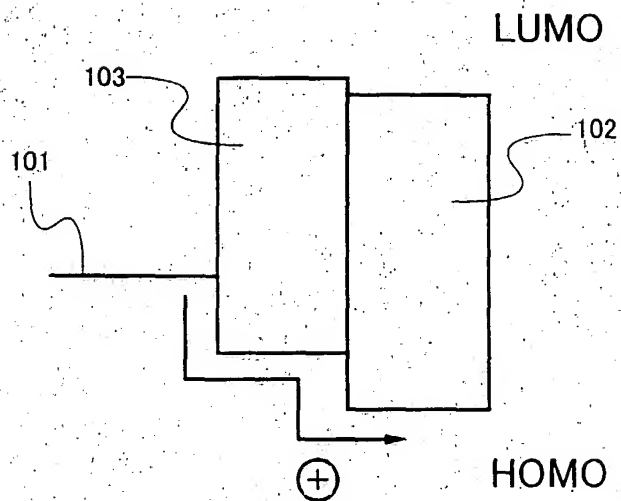


Fig. 2

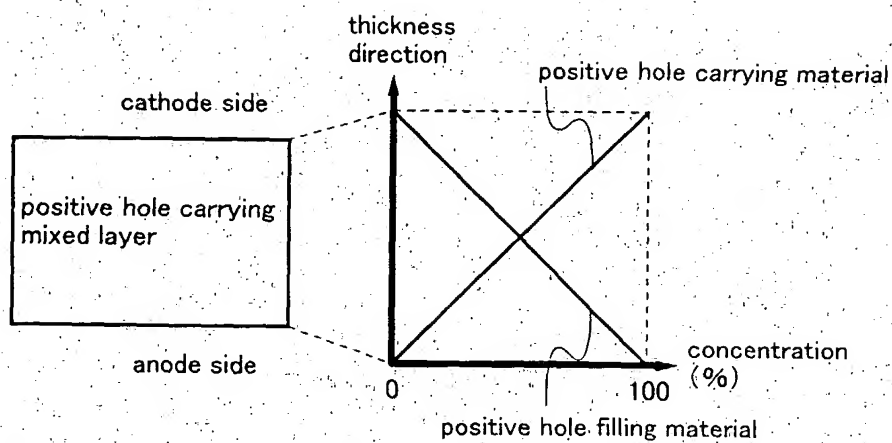


Fig. 3

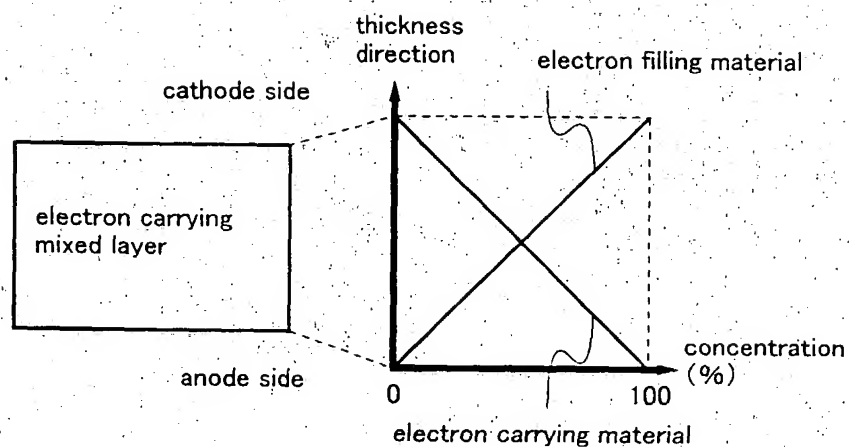


Fig. 4

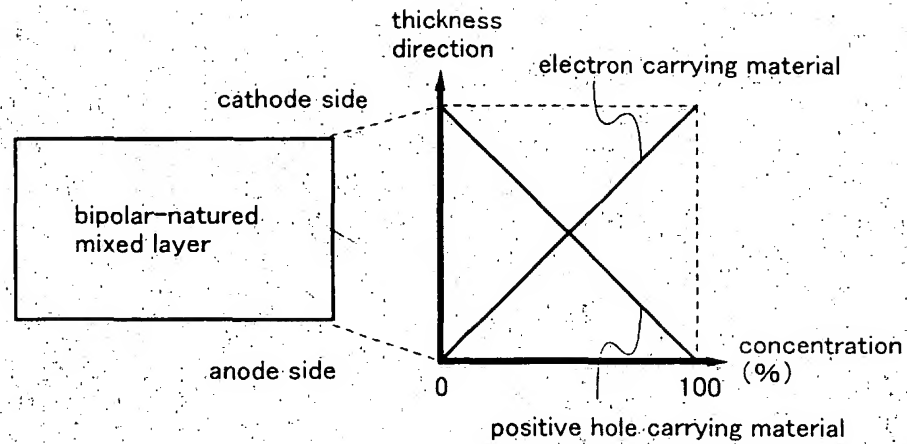


Fig. 5

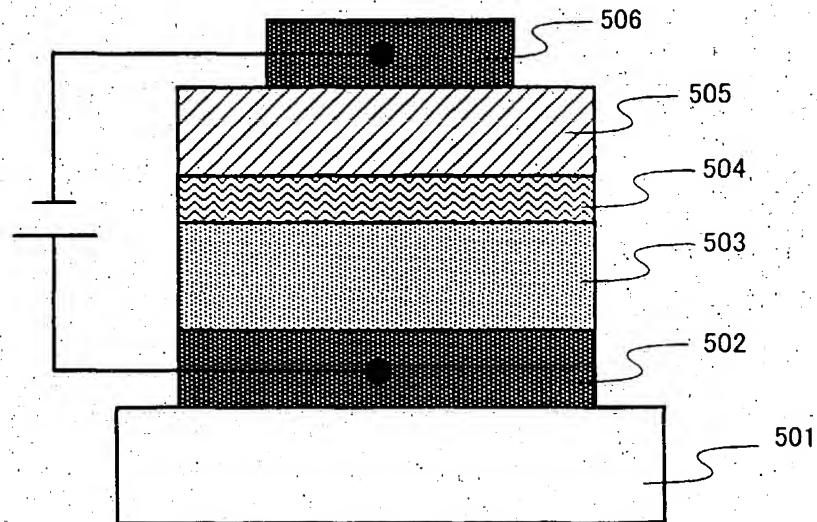


Fig. 6

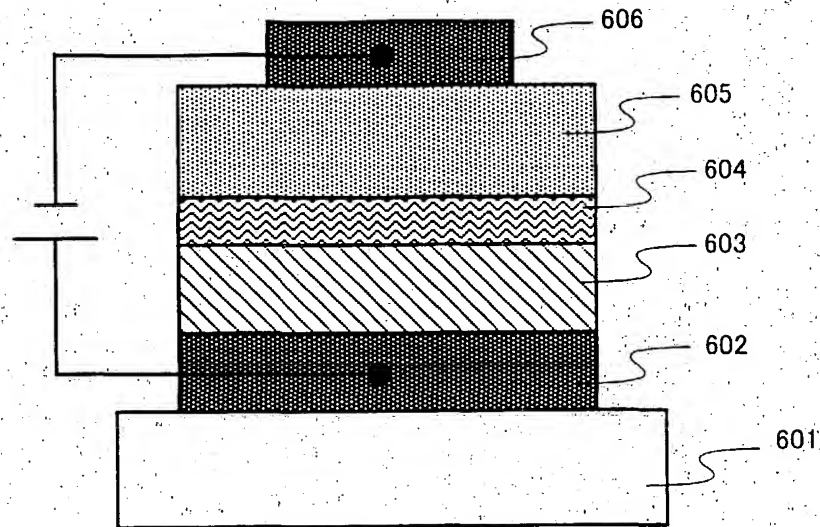


Fig. 7

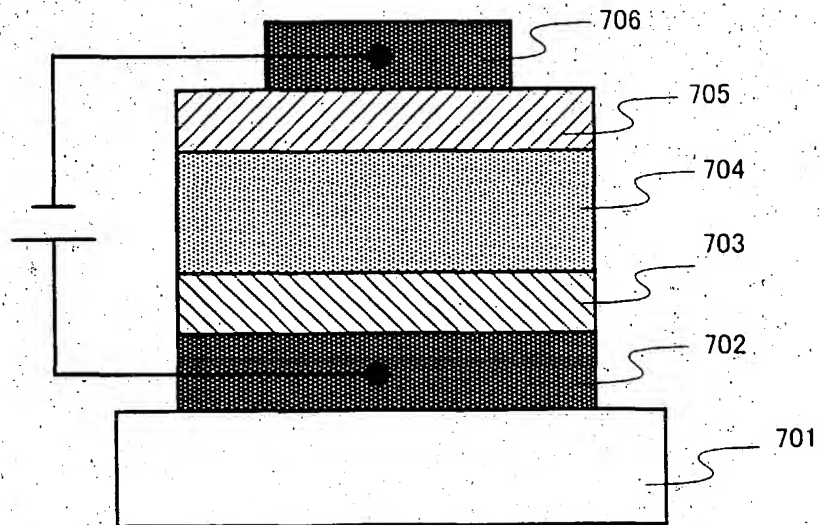


Fig. 8

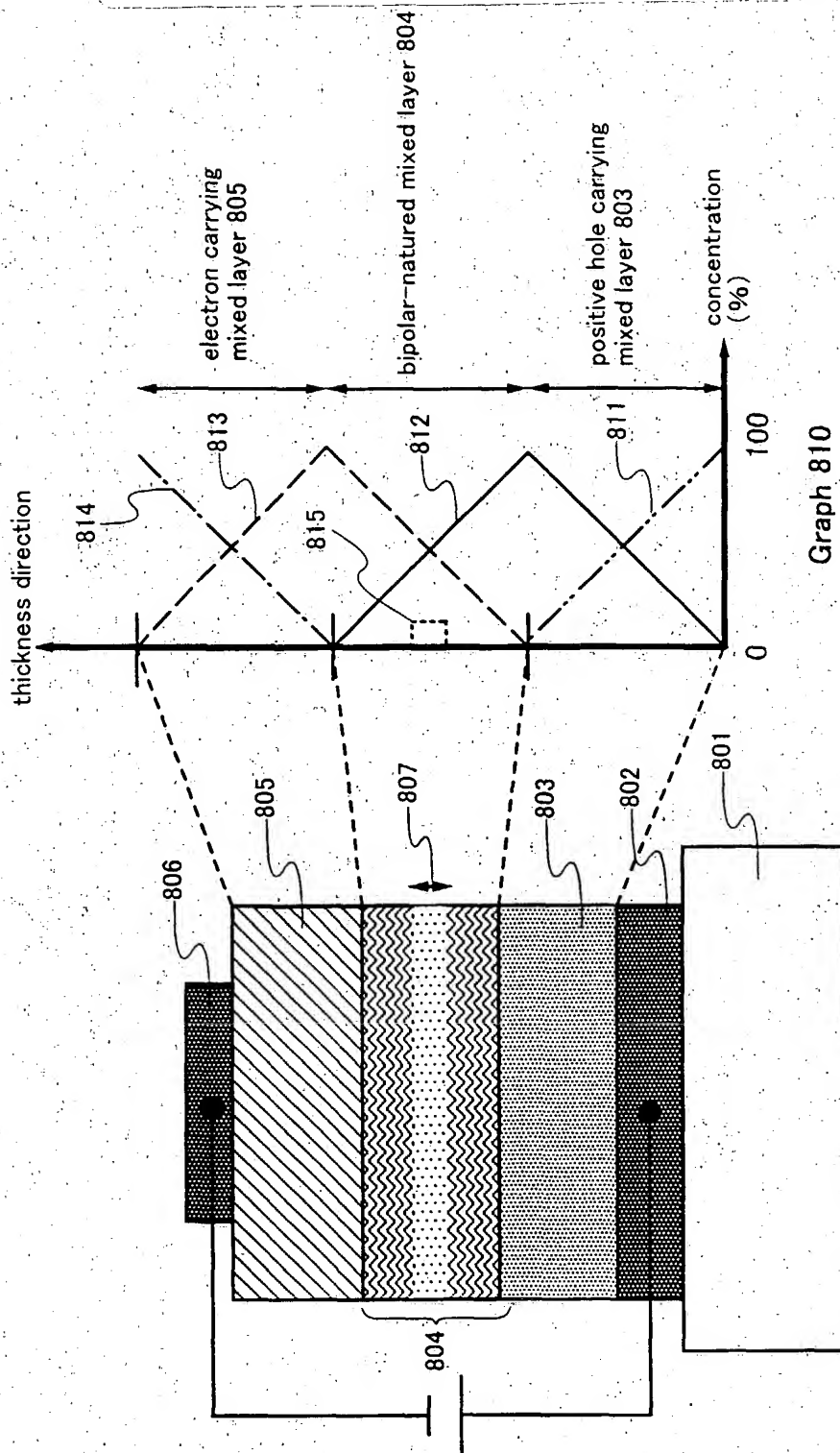


Fig. 9

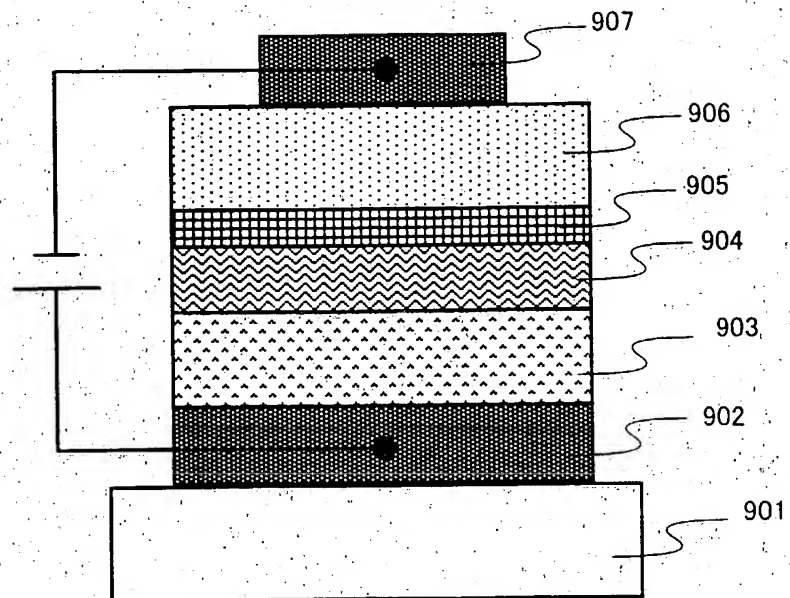


Fig. 10

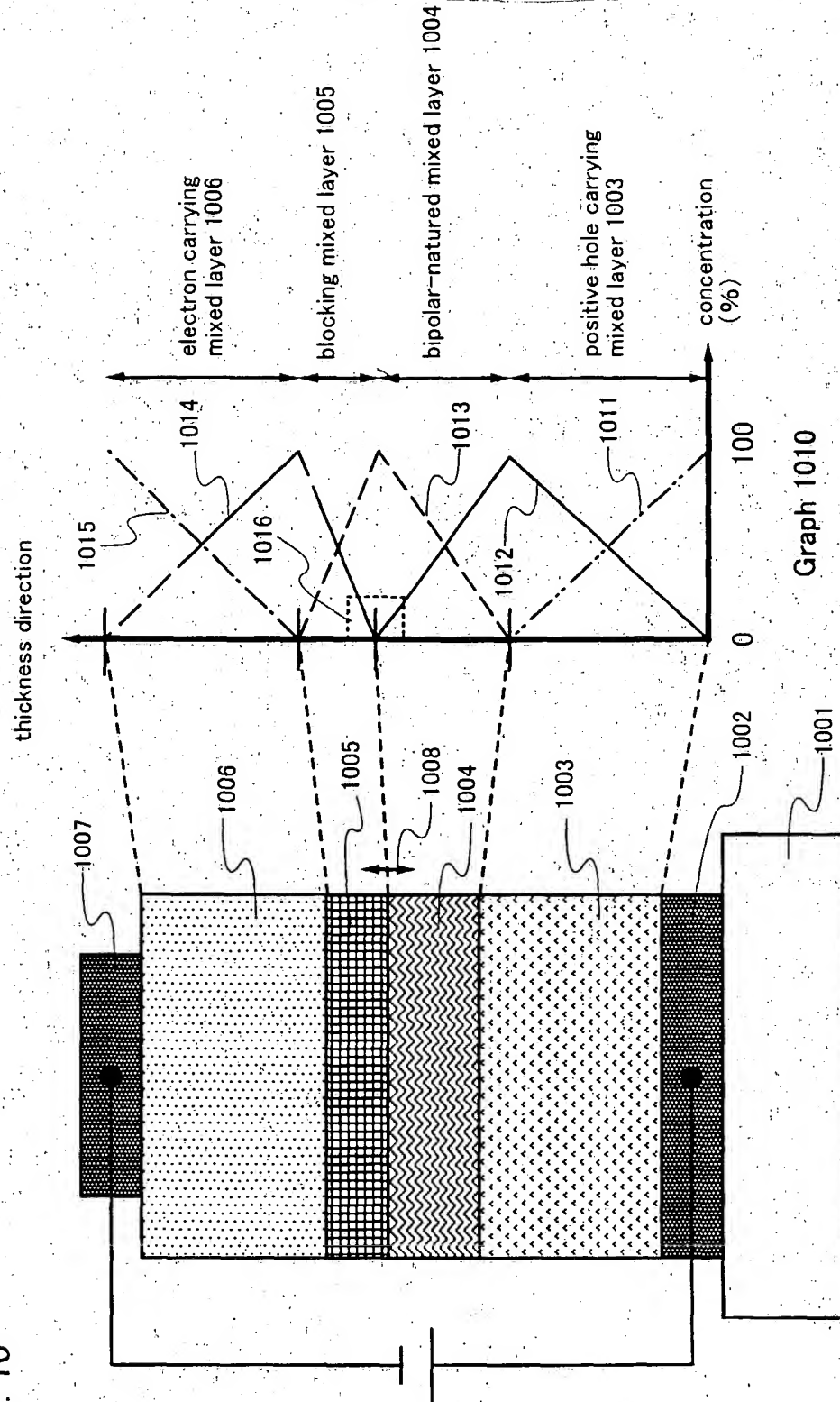
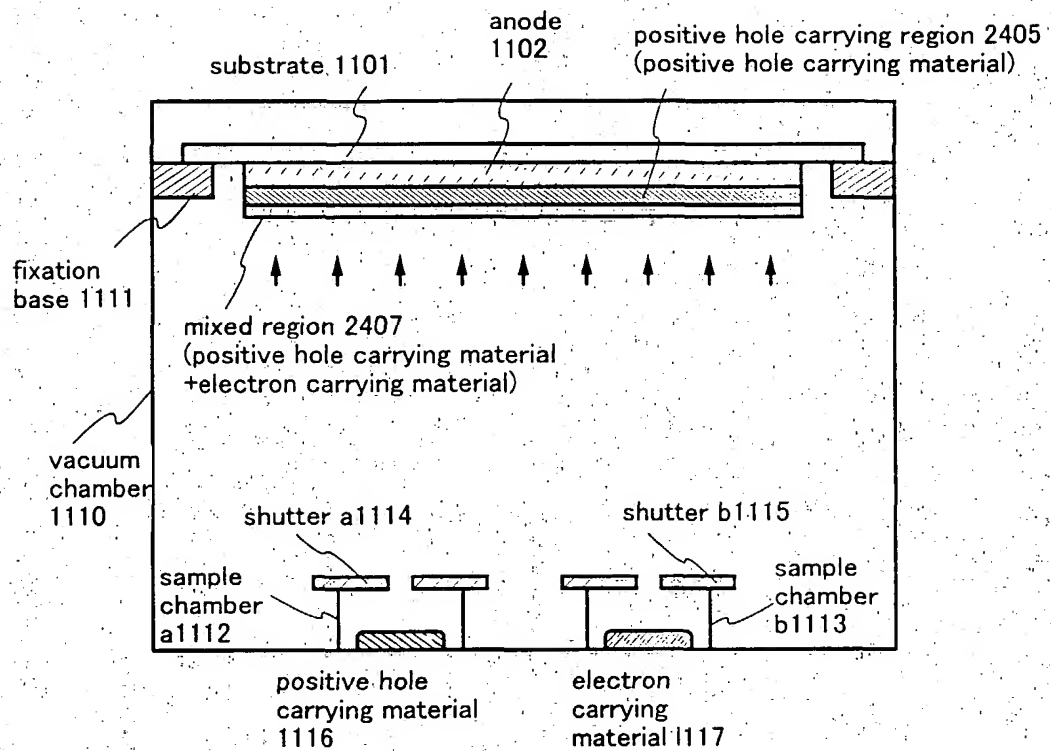


Fig. 11





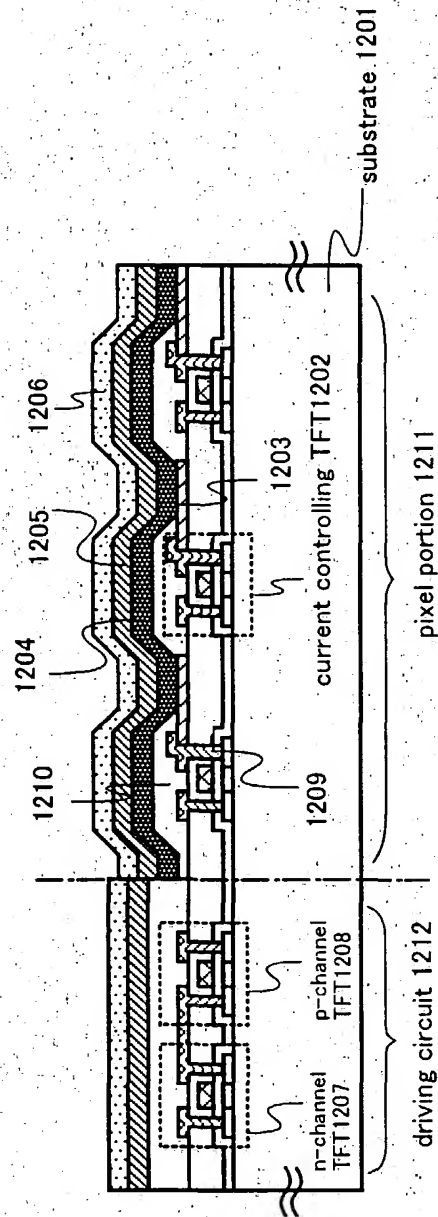


Fig. 12A

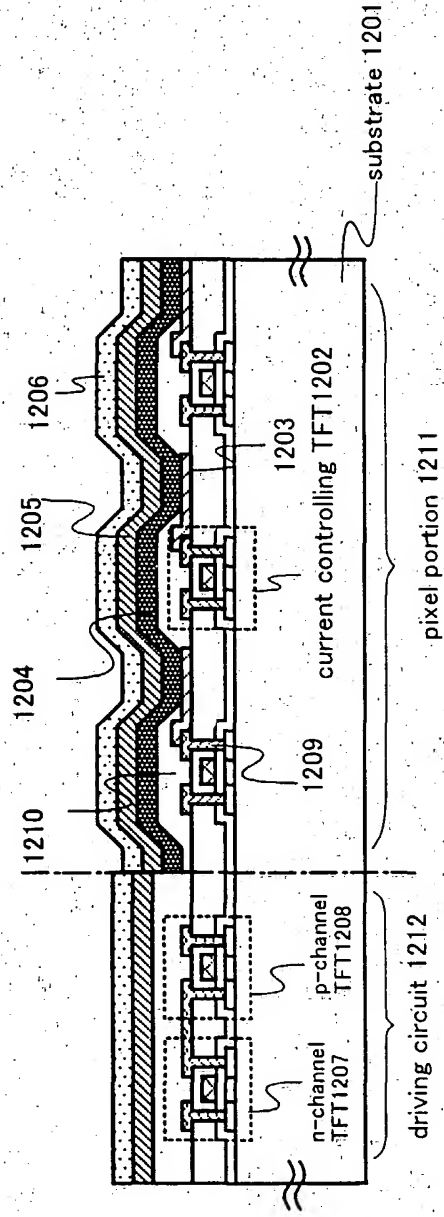


Fig. 12B

A detailed cross-sectional view of a semiconductor device. The device is bounded by two vertical lines labeled 'p' and 'p''. It consists of several layers and components: a top layer 1308, a layer 1305, a central core 1201, a bottom layer 1207, and a base 1304. Various other layers and components are labeled with numbers: 1306, 1306a, 1306b, 1306c, 1309, 1204, 1205, 1206, 1202, 1301, 1203, 1303, and 1208. Two large arrows labeled 'light' point towards the device from the right. The device is shown in a cross-section, revealing its internal structure.

Fig. 14A

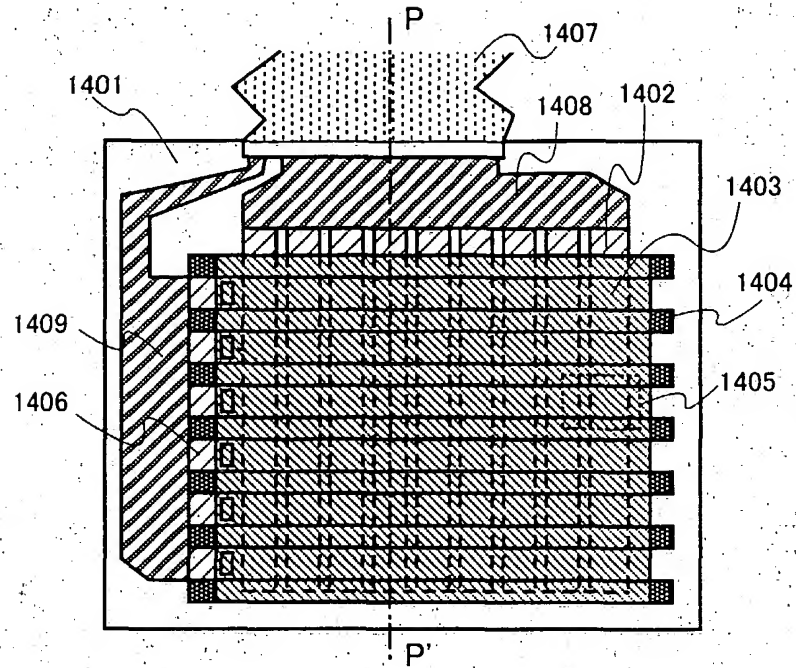


Fig. 14B

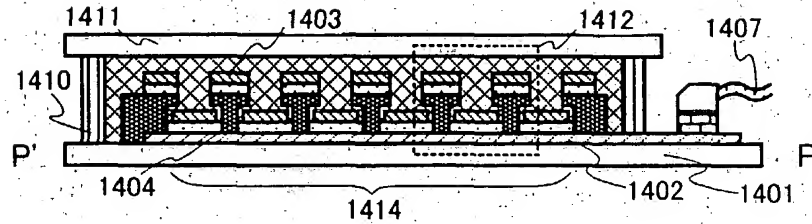


Fig. 14C

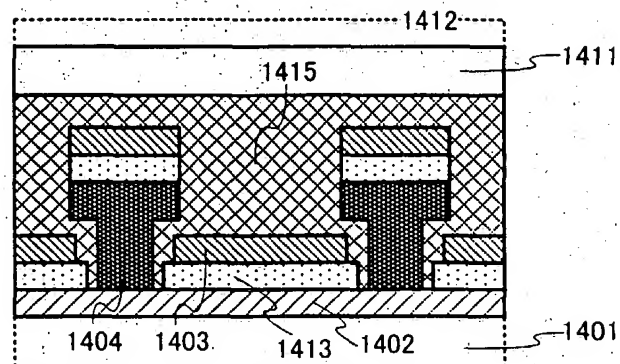


Fig. 15A

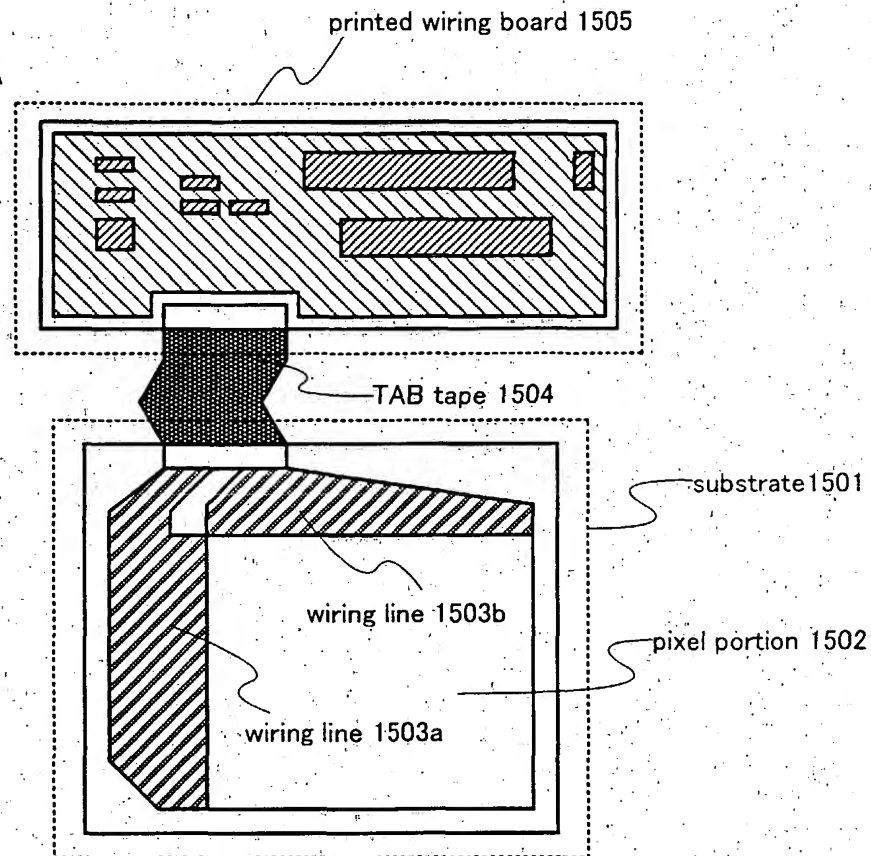


Fig. 15B

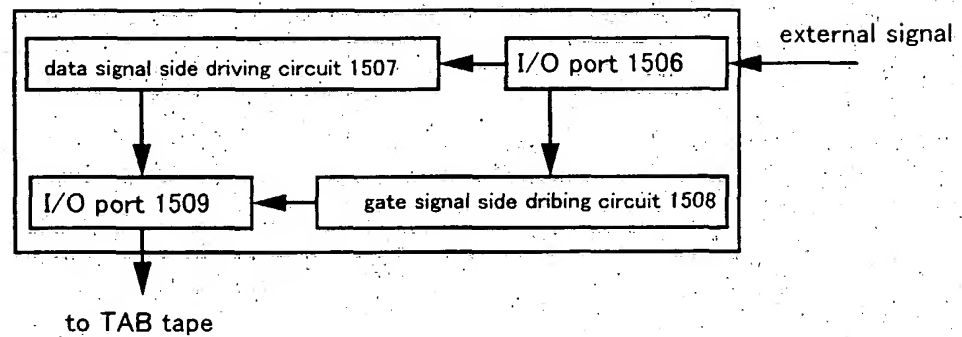


Fig. 16A

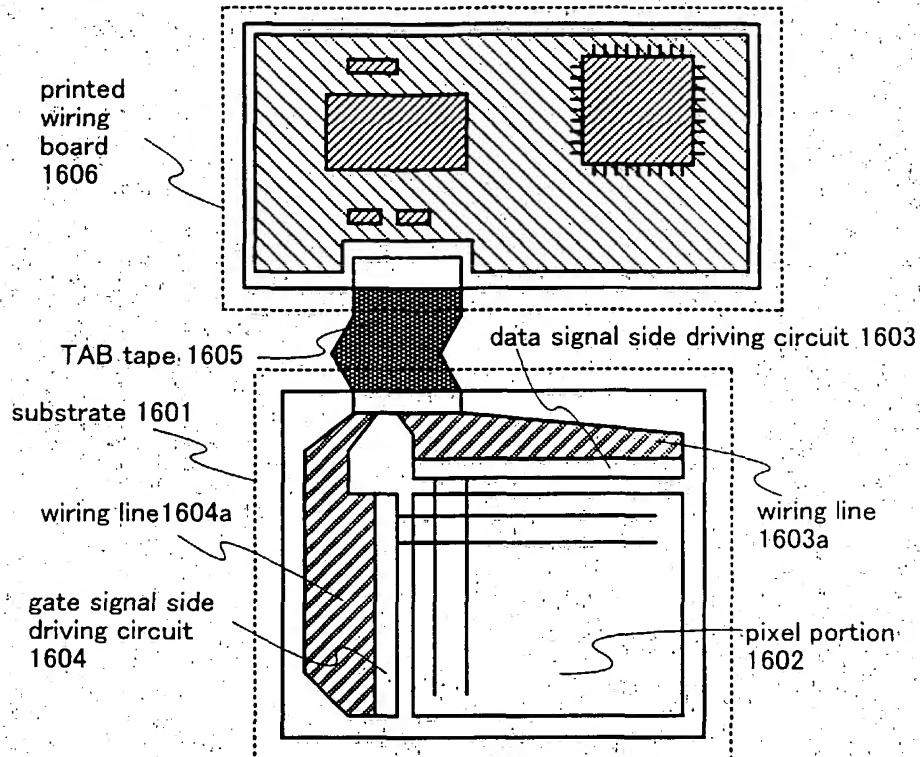
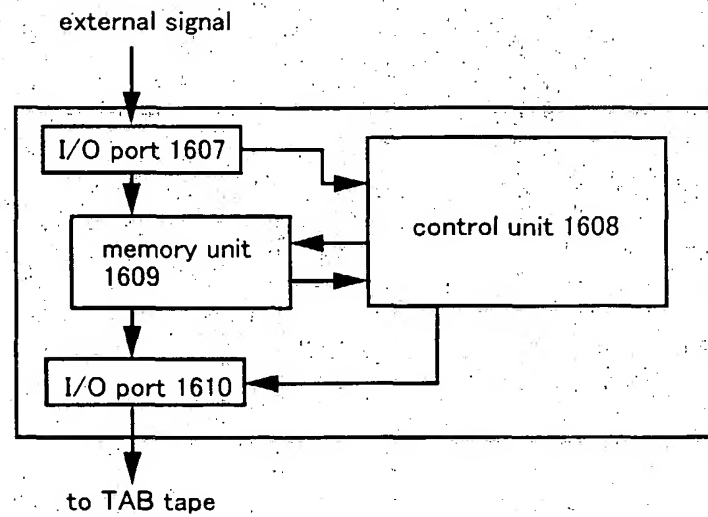


Fig. 16B



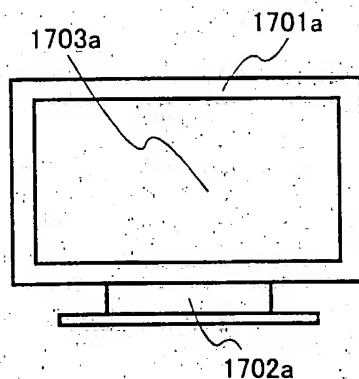


Fig. 17A

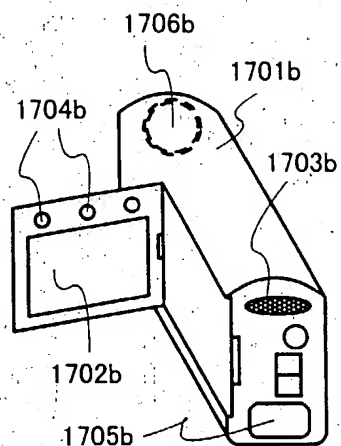


Fig. 17B

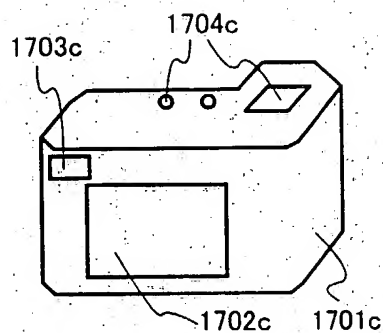


Fig. 17C

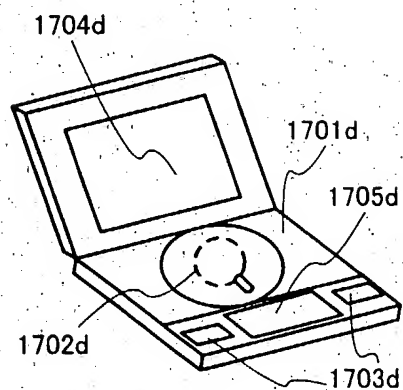


Fig. 17D

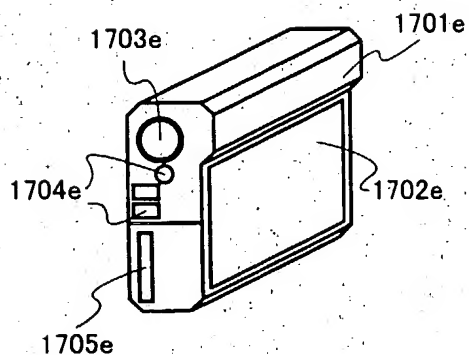


Fig. 17E

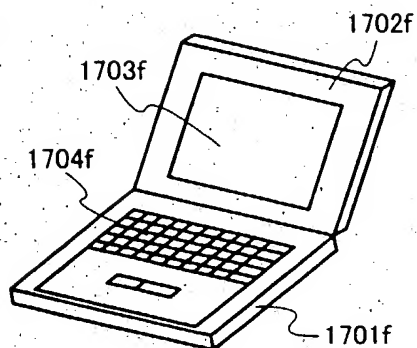


Fig. 17F

Fig. 18A

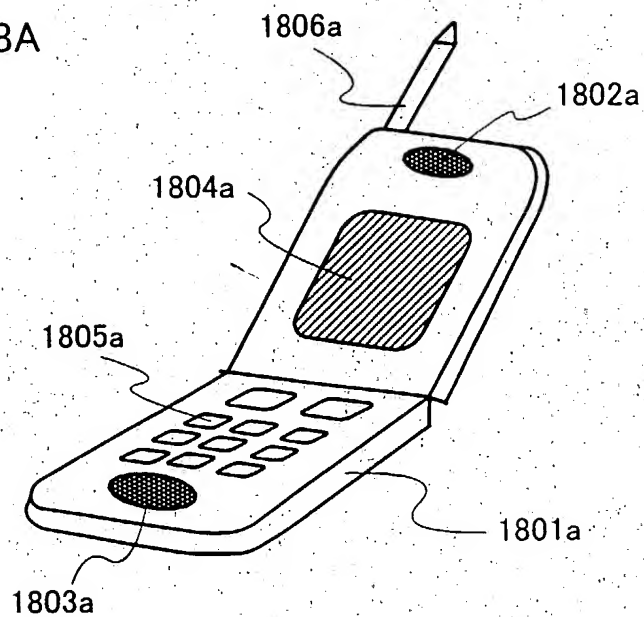
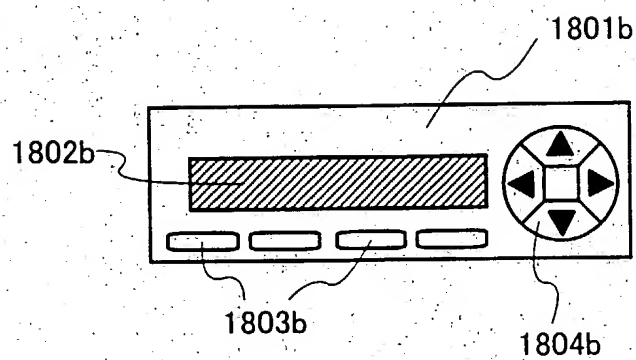


Fig. 18B



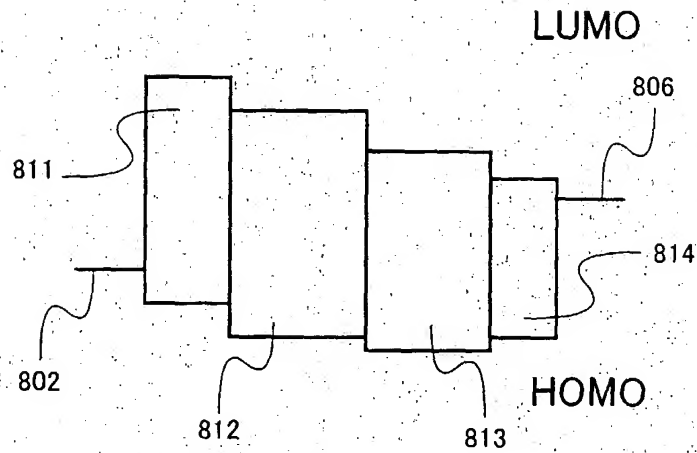


Fig. 19A

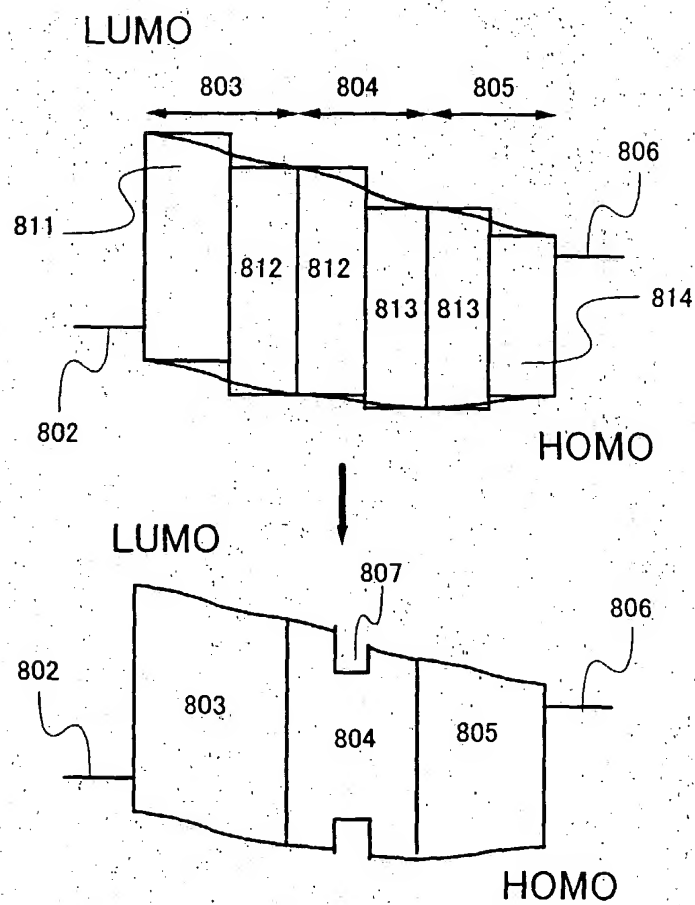


Fig. 19B



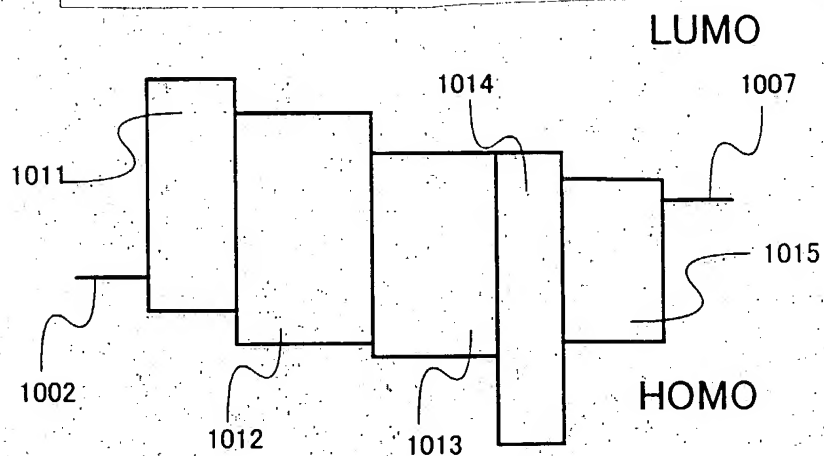


Fig. 20A

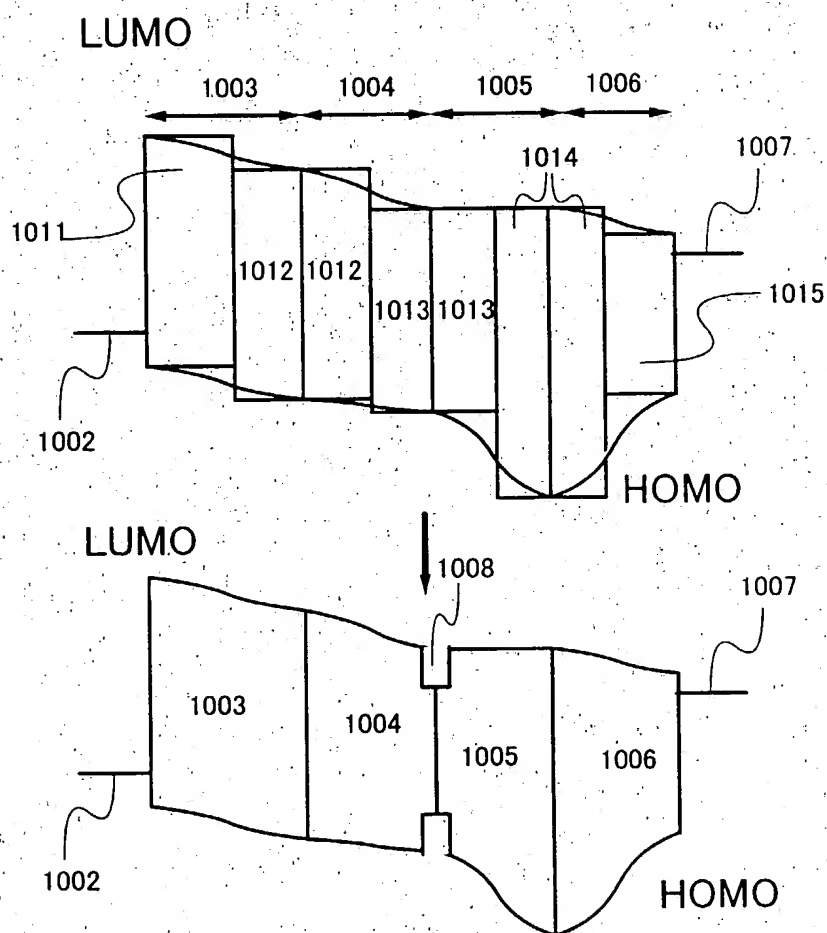
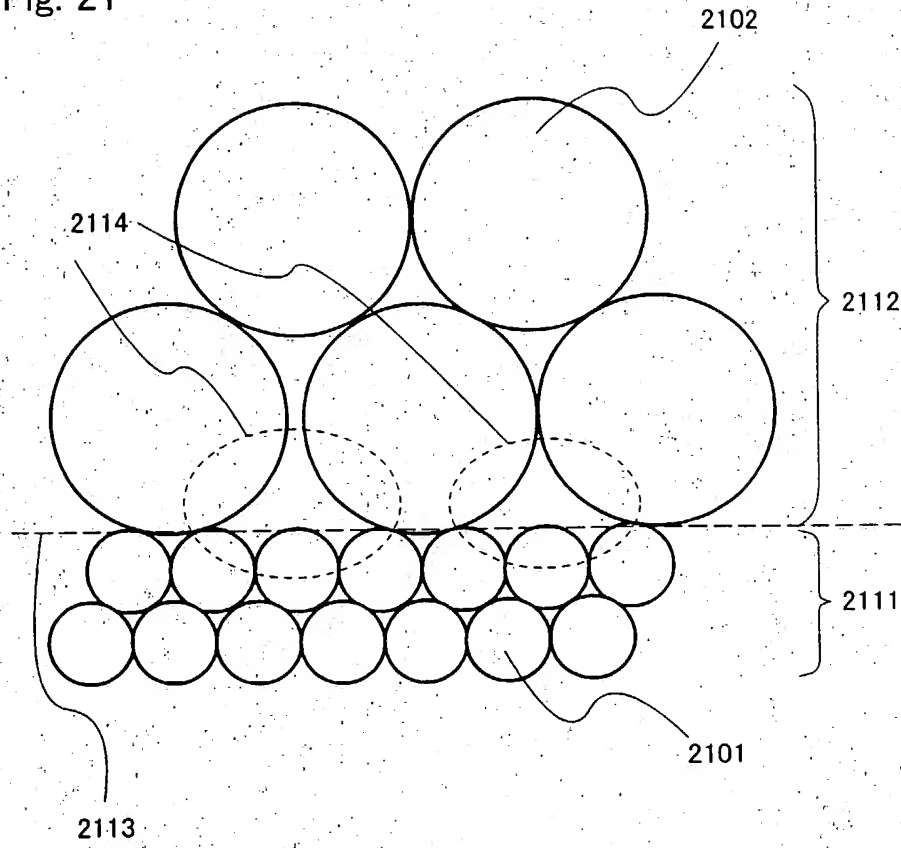


Fig. 20B

Fig. 21



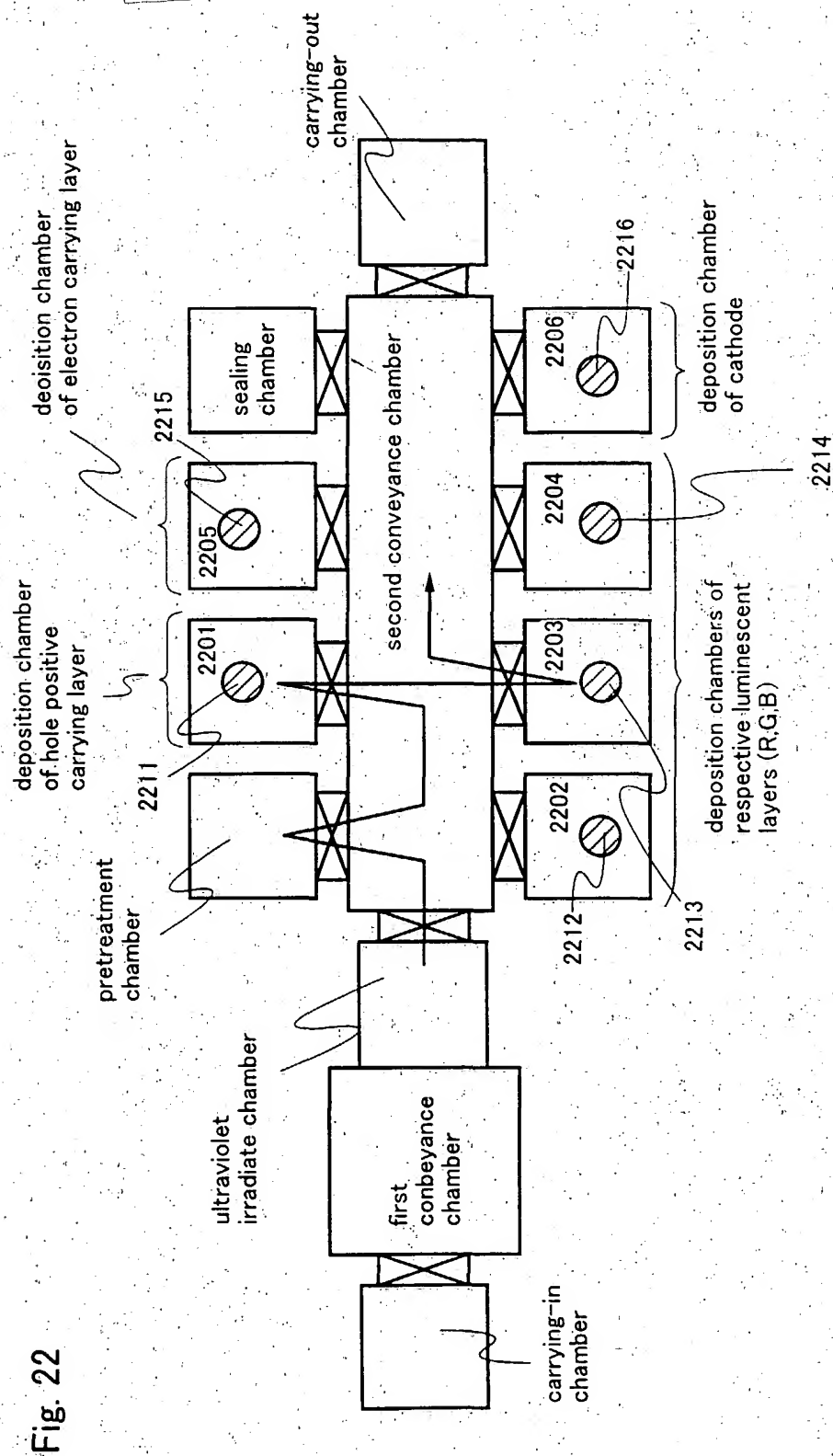


Fig. 23

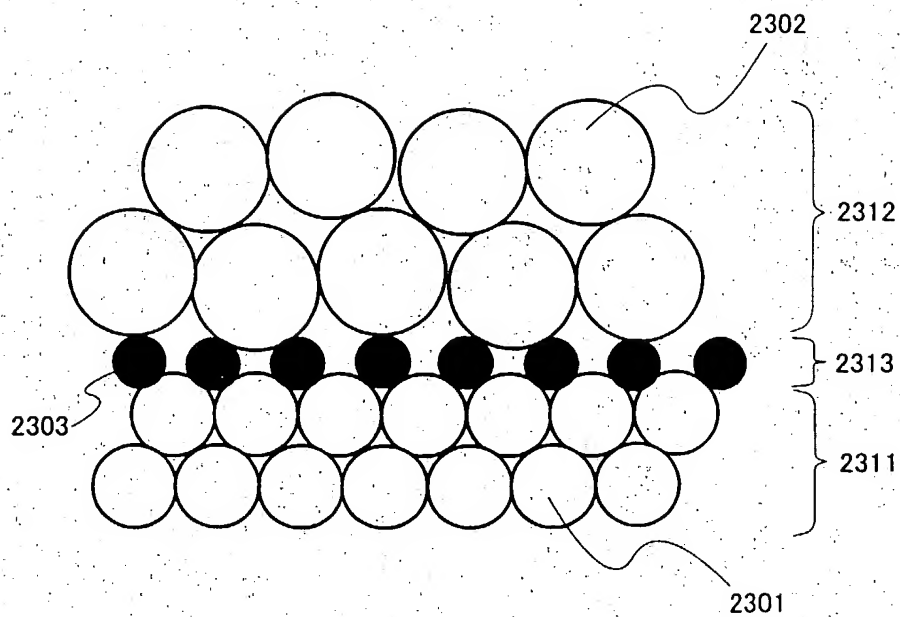


Fig. 24

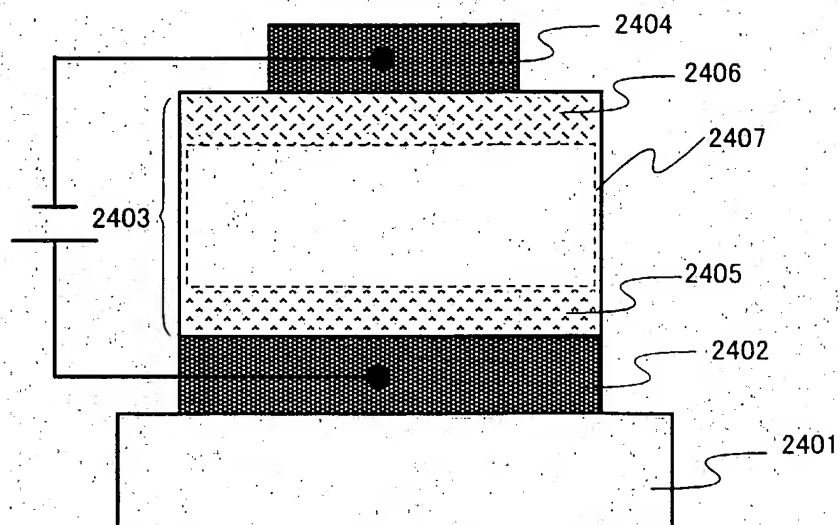


Fig. 25

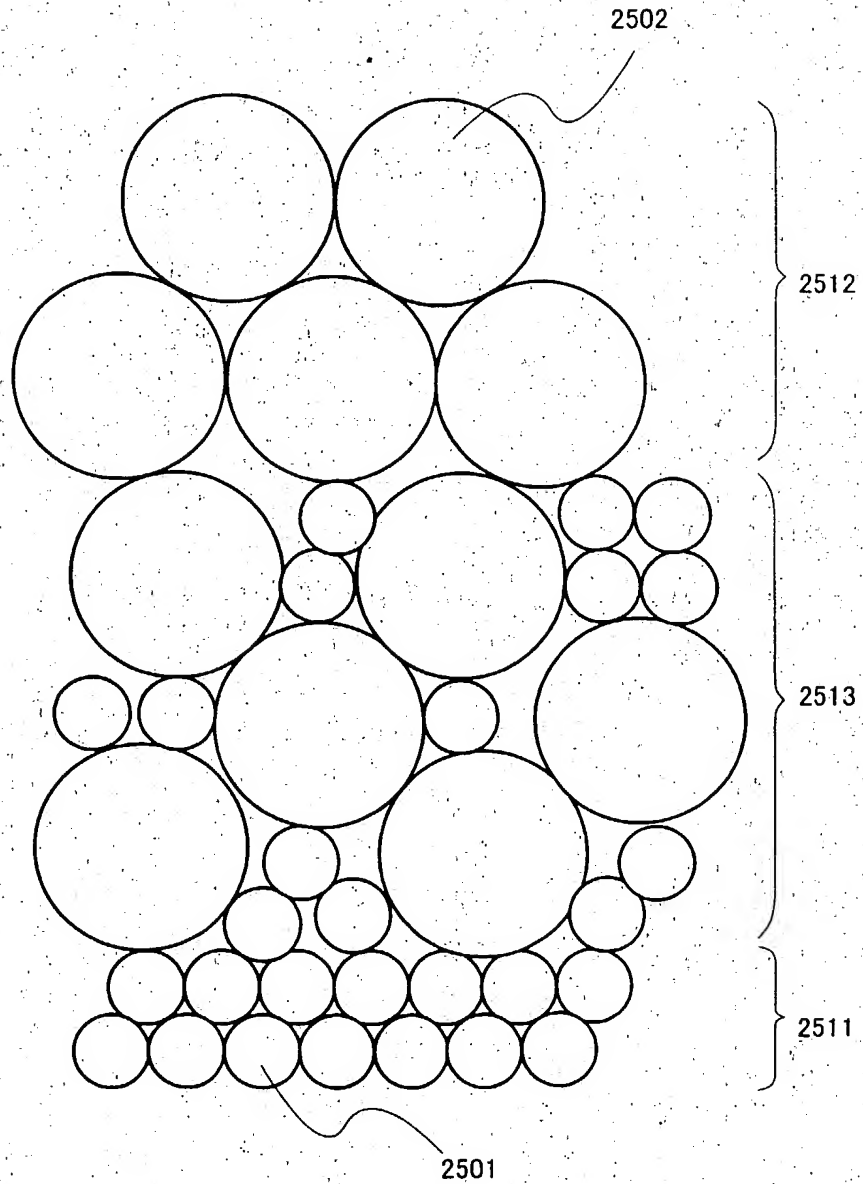


Fig. 26A

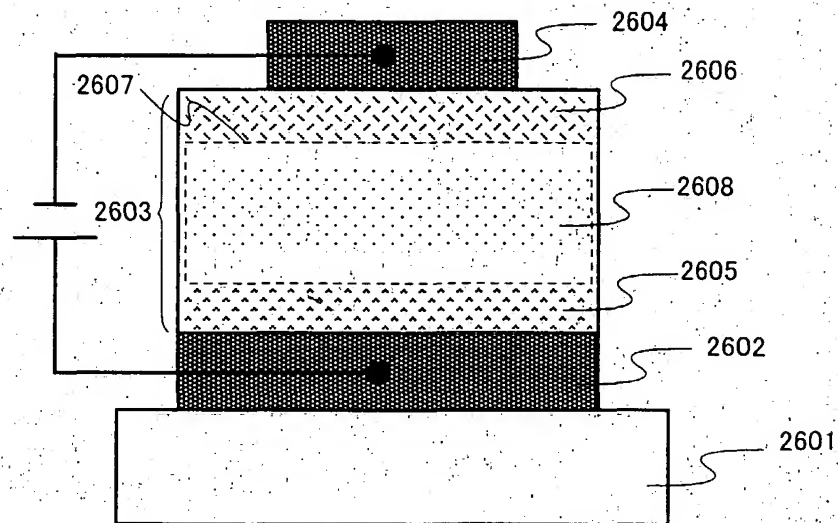


Fig. 26B

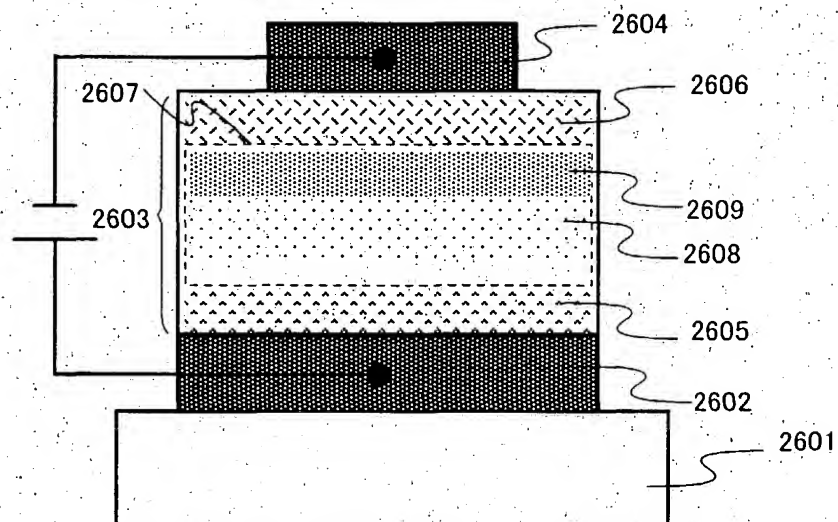


Fig. 27

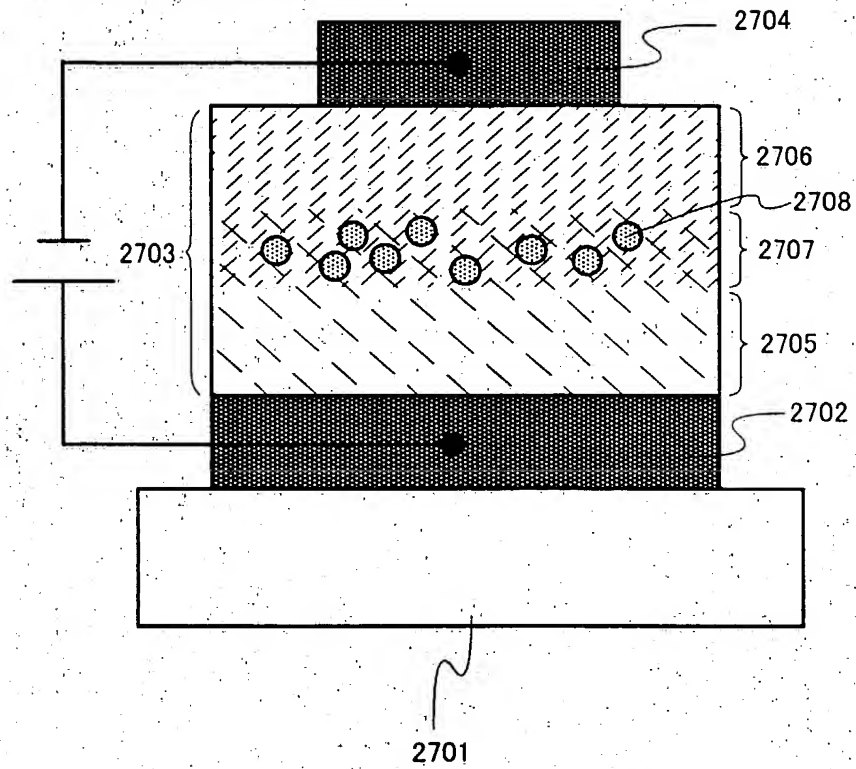




Fig. 28

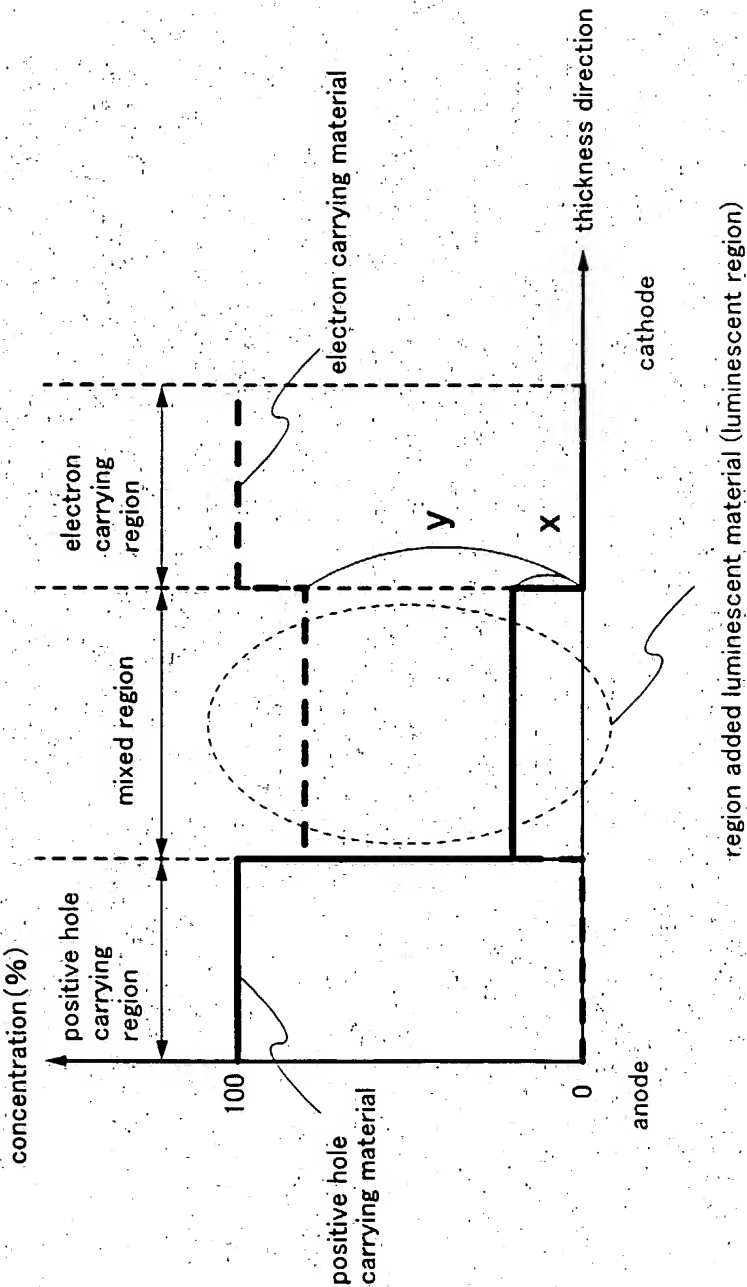


Fig. 29A

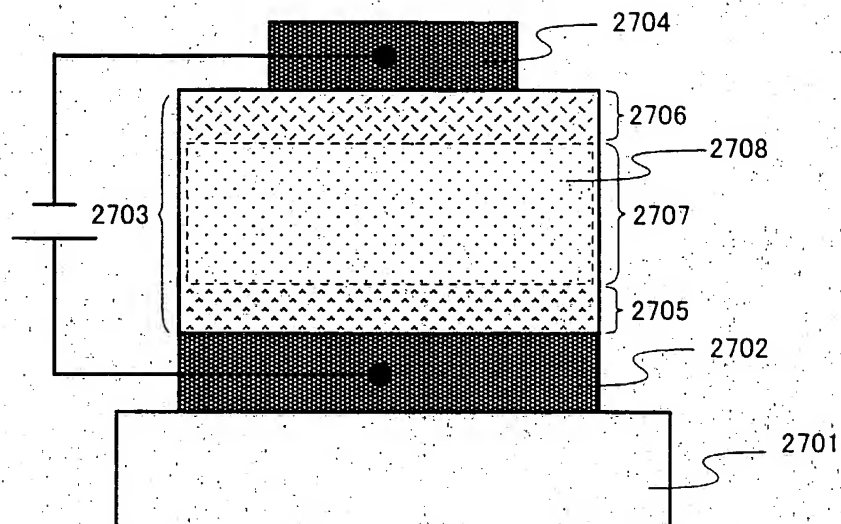


Fig. 29B

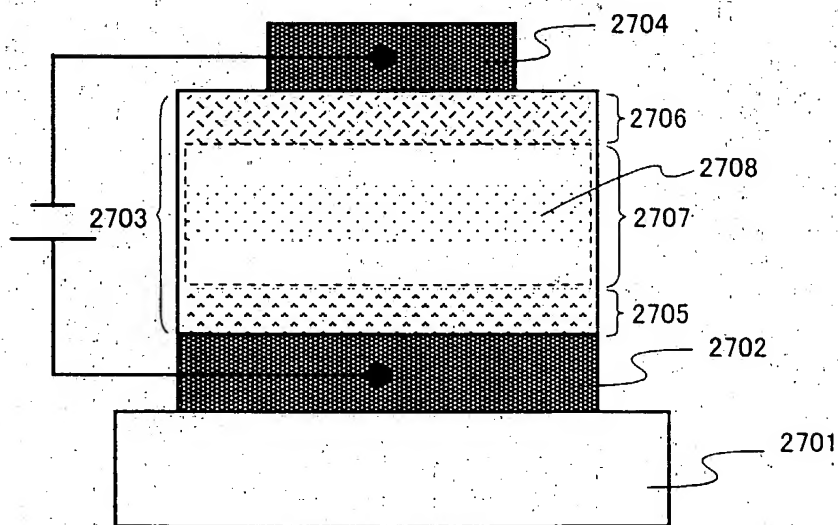


Fig. 30A

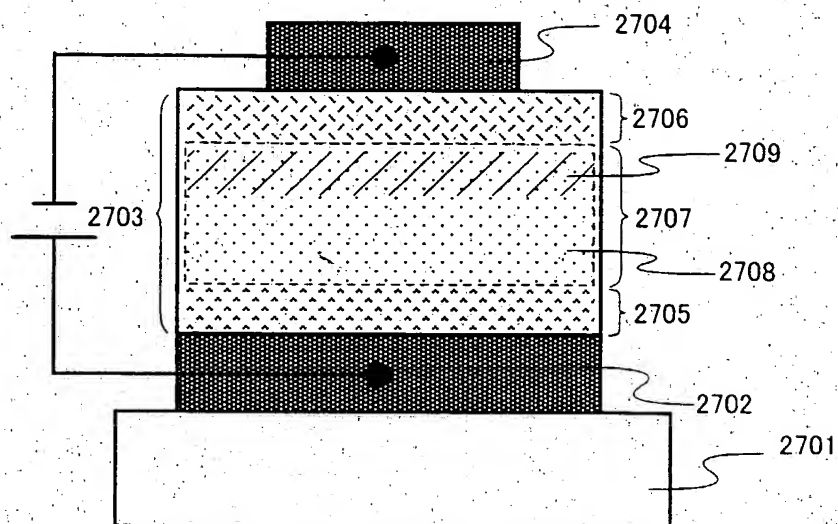


Fig. 30B

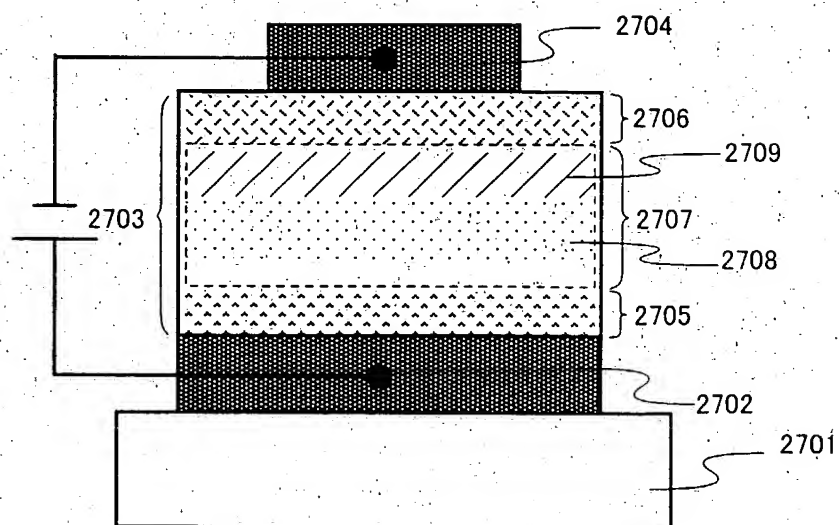


Fig. 31A

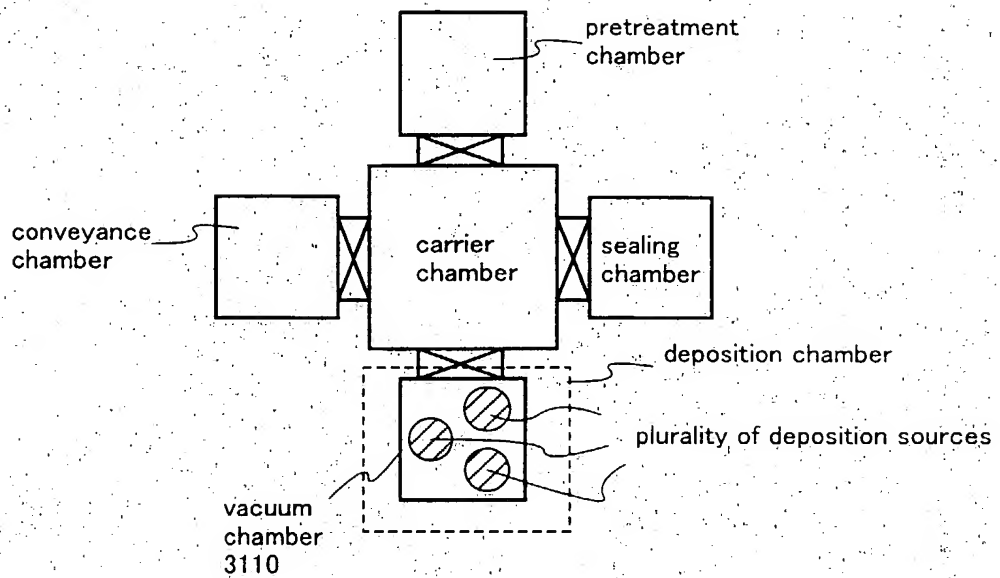


Fig. 31B

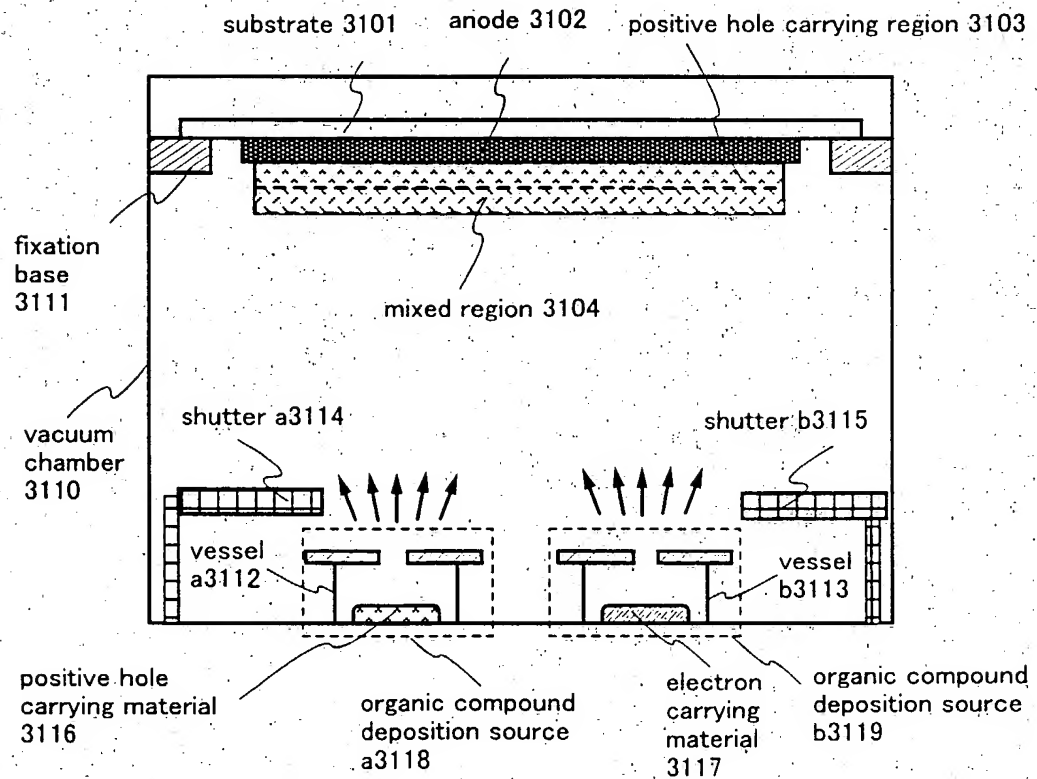


Fig. 32A

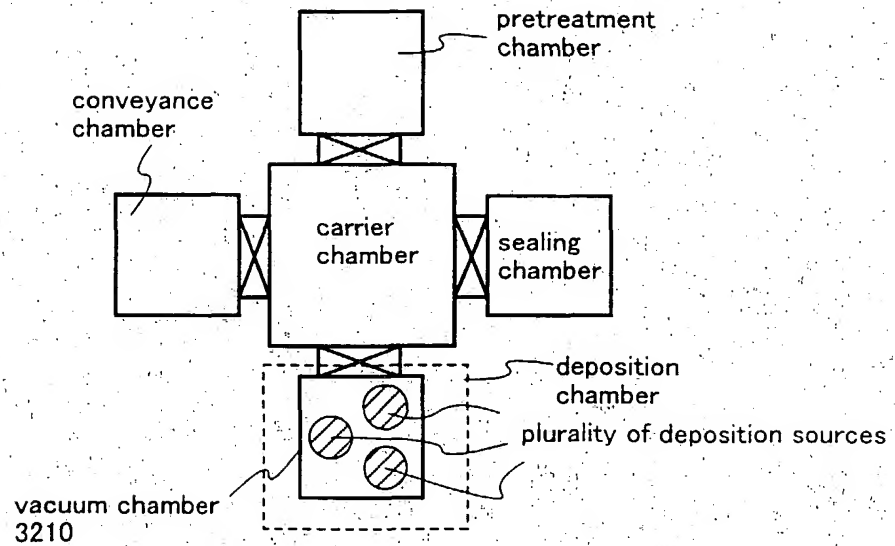


Fig. 32B

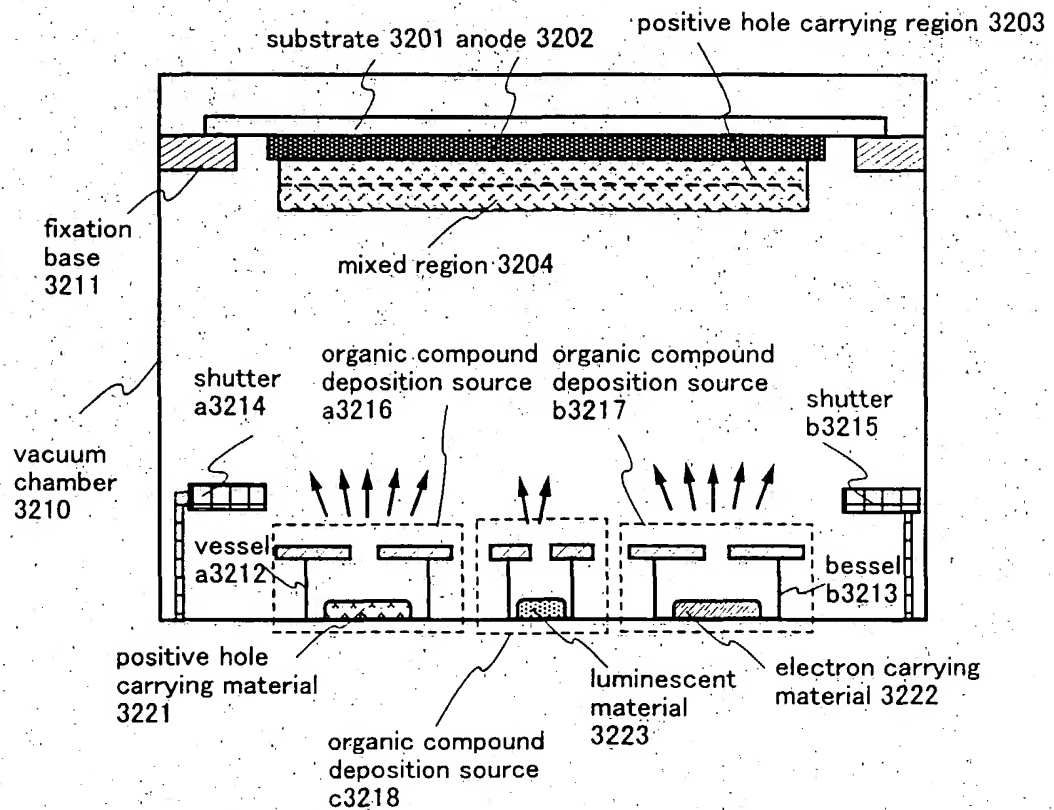


Fig. 33

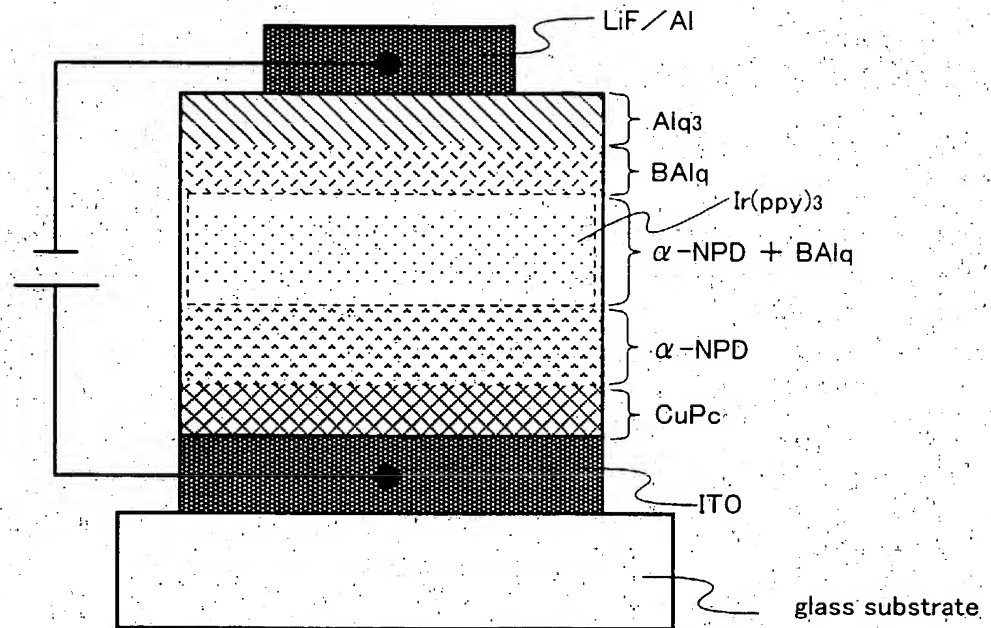


Fig. 34

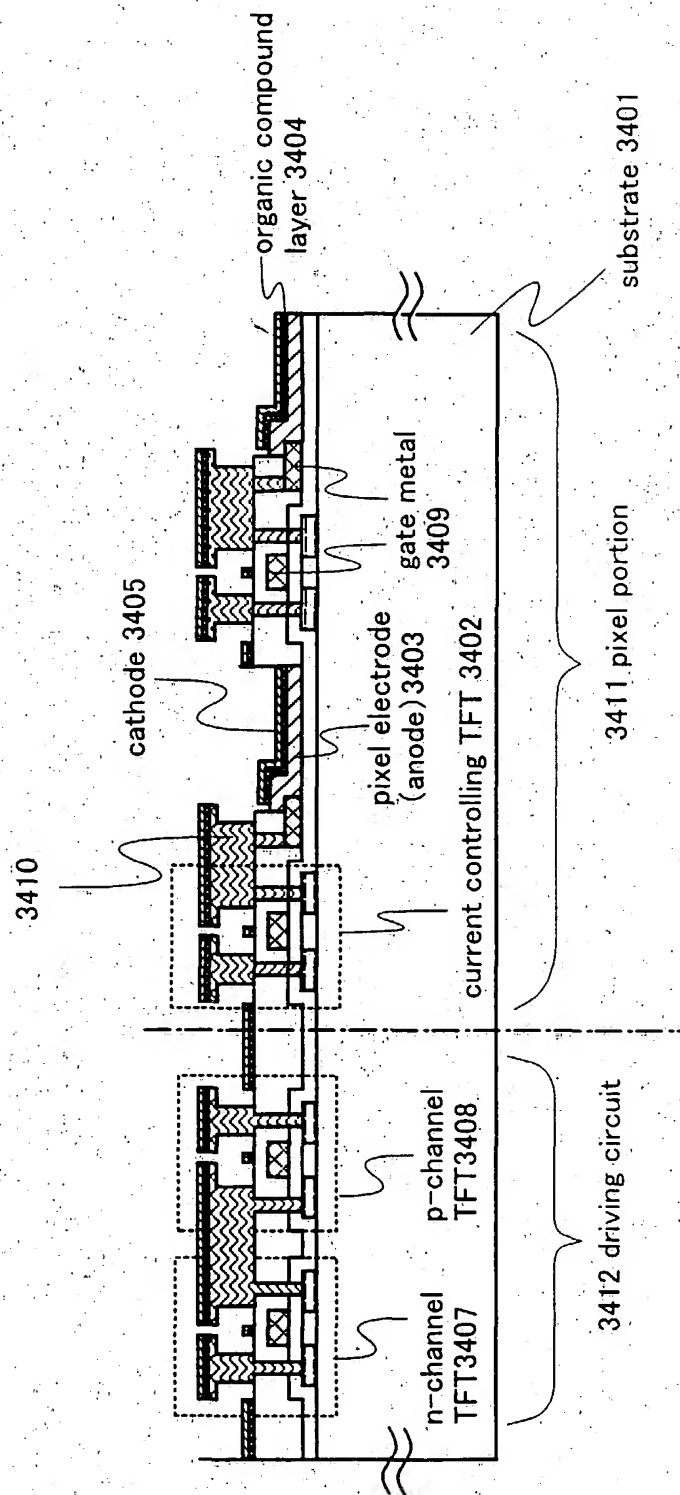
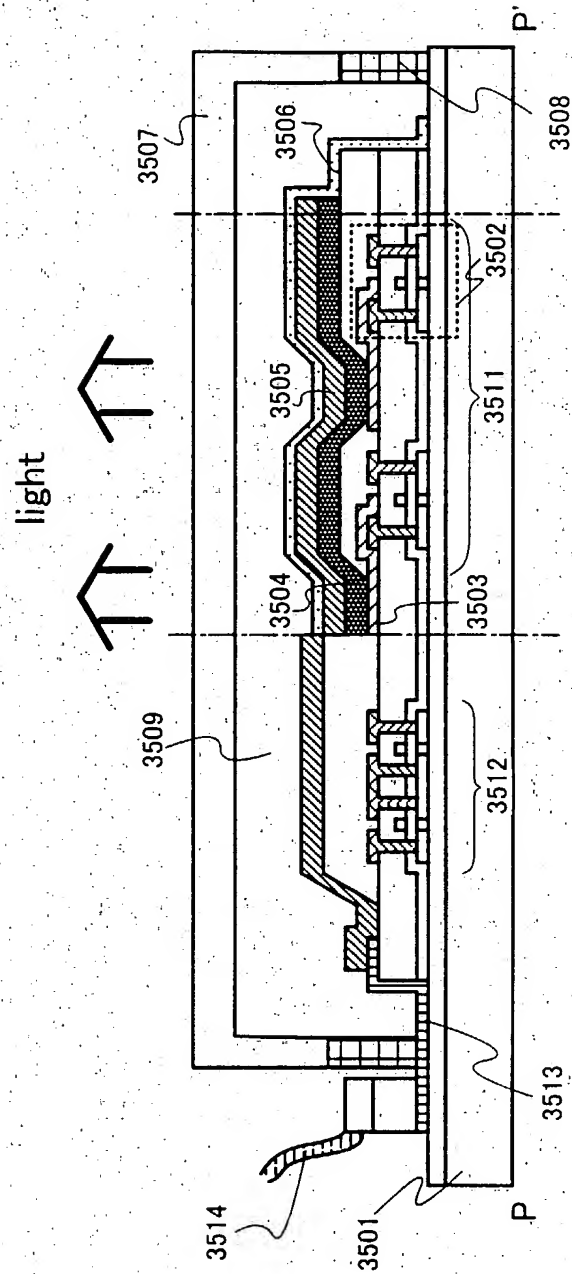


Fig. 35





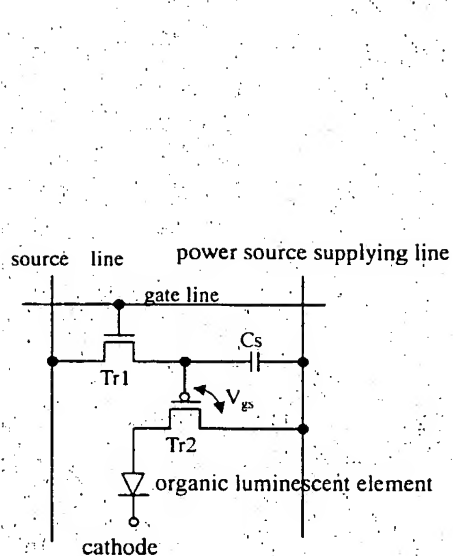


Fig. 36A

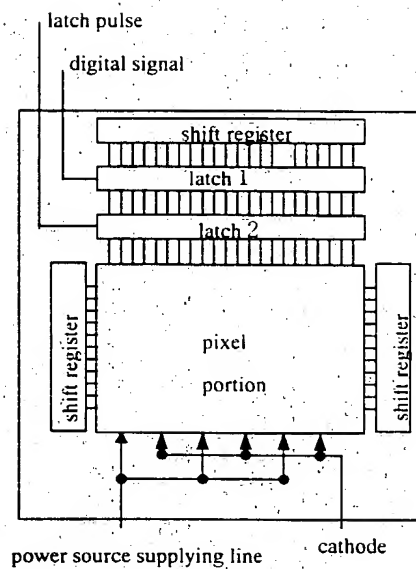
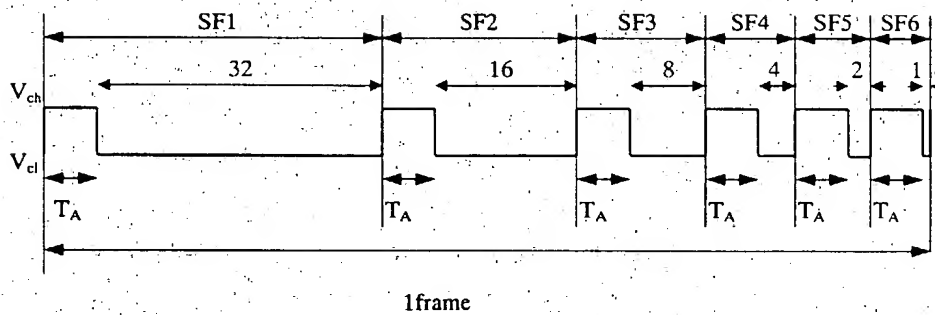


Fig. 36C



SF1-SF6: subframe,  $T_A$ : write in time

Fig. 36B

Fig. 37

